

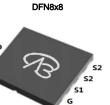
# N-Channel 150V (D-S) MOSFET

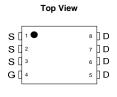
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>		
150	0.006at V <sub>GS</sub> = 10 V	100		
150	0.007at V <sub>GS</sub> = 4.5 V	90		

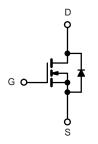
### **FEATURES**

- 175 °C Junction Temperature
- SGT technology Power MOSFET
- Material categorization:









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $(T_C = 2)$	25 °C, unless othe	rwise noted)			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		$V_{GS}$	±20	V	
Continuous Drain Current (T,I = 175 °C)b	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	100		
Continuous Drain Current (1 <sub>J</sub> = 175 °C) <sup>2</sup>	T <sub>C</sub> = 100 °C		60 <sup>a</sup>		
Pulsed Drain Current		I <sub>DM</sub>	300	Α	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	110 <sup>a</sup>		
Avalanche Current		I <sub>AS</sub>	120		
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	110	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	$P_{D}$	136	W	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3 <sup>b</sup> , 8.3 <sup>b, c</sup>	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marian de Lucation to Ambienti	t ≤ 10 sec	D	15	18	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	R <sub>thJA</sub>	40	50	°C/W
Maximum Junction-to-Case	•	R <sub>thJC</sub>	0.85	1.1	

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- $c.\ t \leq 10\ s.$



Parameter	Symbol Test Conditions		Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$ $V_{GS} = 0 \text{ V, } I_{D} = 250  \mu\text{A}$		150	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	2	3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
		V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0 V	-	-	1	μА	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	50		
		V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C	-	-	250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	60	-	-	Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A	-	0.006	-		
Davis Course Co Clata Basistanah	P	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	0.008	-	Ω	
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C	-	0.010	-		
		$V_{GS} = 7.5 \text{ V}, I_D = 30 \text{A}$	-	0.015	-		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A	-	60	-	S	
Dynamic							
Input Capacitance	C <sub>iss</sub>		-	9400	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 150 \text{ V}, f = 1 \text{ MHz}$	ı	470	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	225	-		
Total Gate Charge <sup>c</sup>	Qg		-	75	70		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 150 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$	1	20	-	nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		1	16	-		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	18	27		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 150 \text{ V}, R_{L} = 0.6 \Omega$	-	15	25	nc	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D\cong 50$ A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 $\Omega$	-	35	50	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>		-	-	300	Α	
Diode Forward Voltage	$V_{SD}$	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$	-	1	1.5	٧	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs	-	4	135	ns	

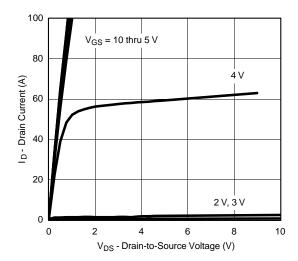
#### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- c. Independent of operating temperature.

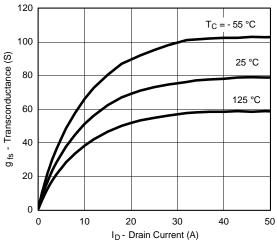
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



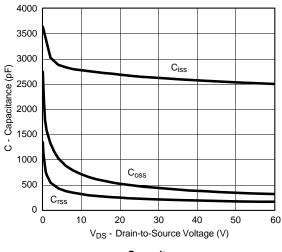
## TYPICAL CHARACTERISTICS (25 °C unless noted)



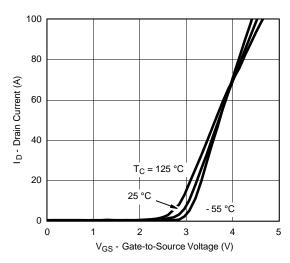
### **Output Characteristics**



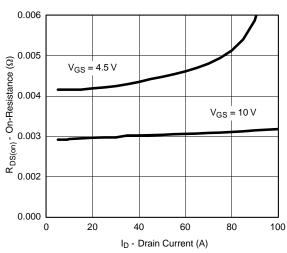
#### Transconductance



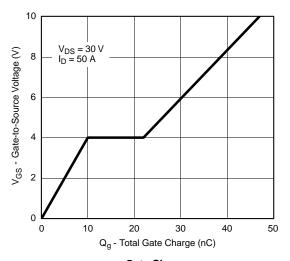
Capacitance



**Transfer Characteristics** 



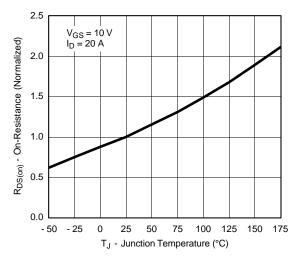
On-Resistance vs. Drain Current



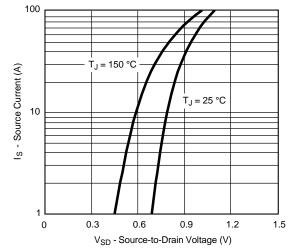
Gate Charge



## TYPICAL CHARACTERISTICS (25 °C unless noted)



On-Resistance vs. Junction Temperature

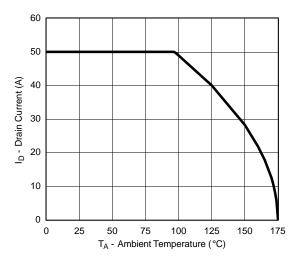


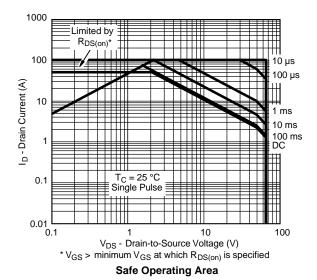
Source-Drain Diode Forward Voltage



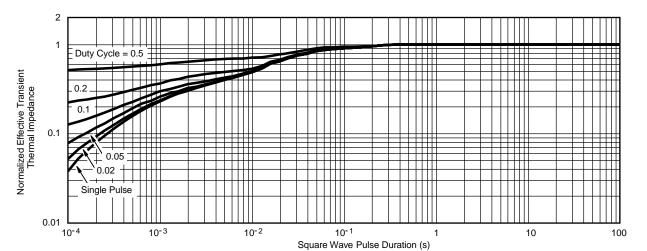
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#### THERMAL RATINGS





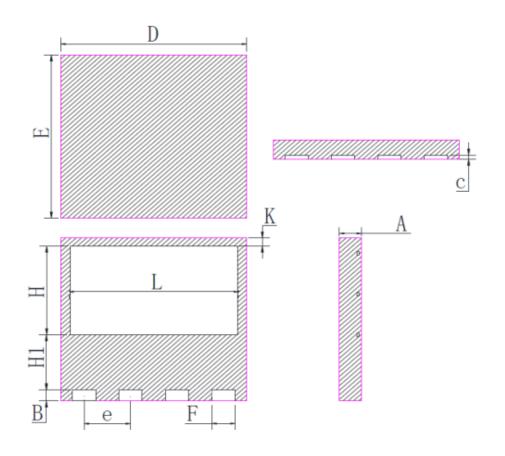
Maximum Drain Current vs. Ambient Temperature



Normalized Thermal Transient Impedance, Junction-to-Case



Package Outline: DFN8X8



Symbol	Min	Тур	Max
A	0.90	0.95	1.00
В	0.45	0.55	0.65
C	0.153	0.203	0.253
D	7.90	8.00	8.10
E	7.90	8.00	8.10
e	1.90	2.00	2.10
F	0.90	1.00	1.10
H	4.20	4.35	4.45
H1	2.60	2.70	2.80
K	0.30	0.40	0.50
L	7.10	7.20	7.30

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