

NTJD4401NT1G Dual N-Channel 20 V (D-S) MOSFET

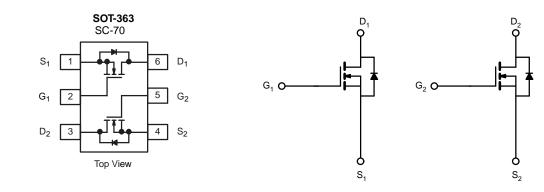
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
	0.086 at V _{GS} = 4.5 V	2.6 ^a				
20	0.110 at V _{GS} = 2.5 V	2.5 ^a	5.0 nC			
	0.180 at V _{GS} = 1.8 V	2.3 ^a				

FEATURES

- Halogen-free According to IEC 61249-2-21 • Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Typical ESD Protection 2100 V HBM
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

· Load Switch for Portable Applications



ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, unle	ess otherwise not	ed)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	20	V		
Gate-Source Voltage	V _{GS}	± 12	V		
	T _C = 25 °C		2.6 ^a		
Continuous Drain Current (T ₁ = 150 °C)	T _C = 70 °C		2.2 ^a		
Continuous Drain Current $(T_J = 150 \text{ C})$	T _A = 25 °C	I _D	2.3 ^{a, b, c}		
	T _A = 70 °C		1.8 ^{b, c}	A	
Pulsed Drain Current	I _{DM}	8			
Continuous Source-Drain Diode Current	T _C = 25 °C	1	2.3		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.10 ^{b, c}		
	T _C = 25 °C		2.70		
Maximum Power Dissinction	T _C = 70 °C	D	1.70	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	1.5 ^{b, c}	vv	
	T _A = 70 °C		1.0 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	130	170	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	80	100			

Notes: a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s. d. Maximum under steady state conditions is 220 °C/W. COMPLIANT

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static		· · · · · · · · · · · · · · · · · · ·				
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		20		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 2.3		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.5		2.0	V
Coto Source Lookage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 25	μA
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 4.5 \text{ V}$			1	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA
Zero Gale voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	4			А
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 1 \text{ A}$		0.086		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 1 A		0.110		
		$V_{GS} = 1.8 \text{ V}, I_D = 0.2 \text{ A}$		0.180		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 4 V, I _D = 1.5 A		4		S
Dynamic ^b						-
Tatal Cata Charge	Qg	V_{DS} = 10 V, V_{GS} = 8 V, I_{D} = 1.5 A		5.0		nC
Total Gate Charge				3.0		
Gate-Source Charge	Q _{gs}	$V_{\rm DS}$ = 10 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 1.5 A		1.0		
Gate-Drain Charge	Q _{gd}			2.0		
Gate Resistance	Rg	f = 1 MHz	0.4	1.9	3.8	kΩ
Turn-On Delay Time	t _{d(on)}			43	65	- ns
Rise Time	t _r	V_{DD} = 10 V, R_L = 8.3 Ω		80	120	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 1.2 A, V_{GEN} = 4.5 V, R_g = 1 Ω		480	720	
Fall Time	t _f			220	330	
Turn-on Delay Time	t _{d(on)}			22	33	
Rise Time	tr	V_{DD} = 10 V, R_L = 8.3 Ω		46	70	
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong {\rm 1.2~A,~V_{\rm GEN}} = {\rm 8~V,~R_g} = {\rm 1~\Omega}$		645	968	
Fall Time	tr			215	323	
Drain-Source Body Diode Characteristic	s	·				
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C		2.6		
Pulse Diode Forward Current	I _{SM}			4		A
Body Diode Voltage	V _{SD}	$I_{\rm S}$ = 1.2 A, $V_{\rm GS}$ = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			9	18	ns
Body Diode Reverse Recovery Charge	Body Diode Reverse Recovery Charge Q.,			2	4	nC
Reverse Recovery Fall Time	ta	I _F = 1.2 A, dl/dt = 100 A/µs, T _J = 25 °C		5		
Reverse Recovery Rise Time	t _b	1 1		4		ns

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

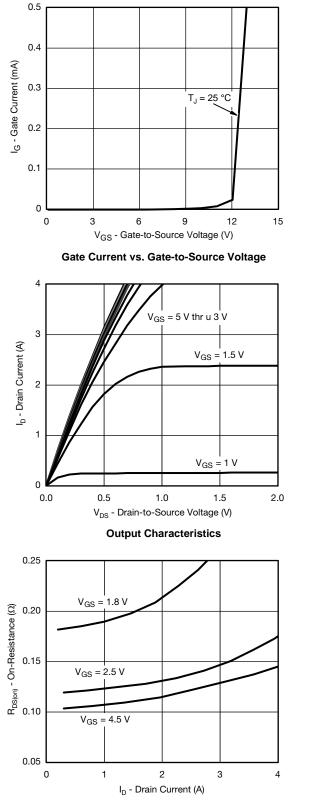
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

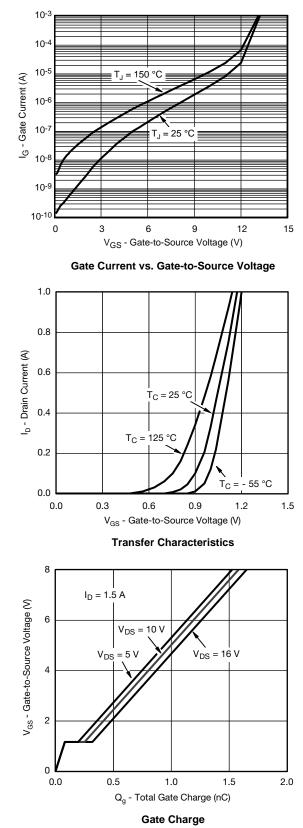
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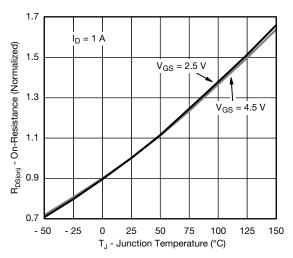
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Drain Current

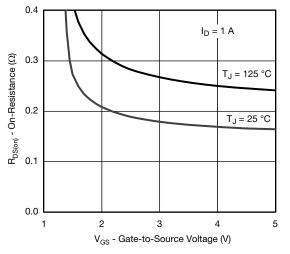




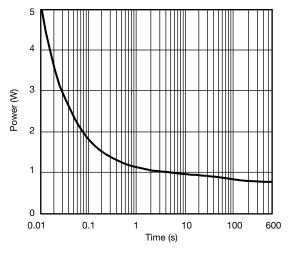


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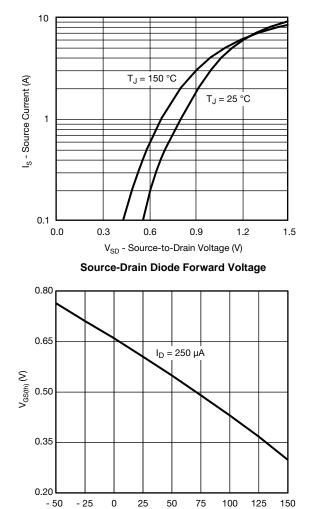




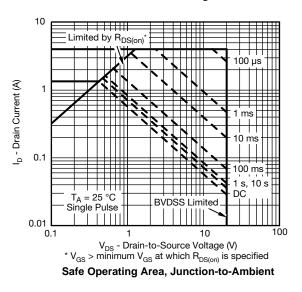
On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

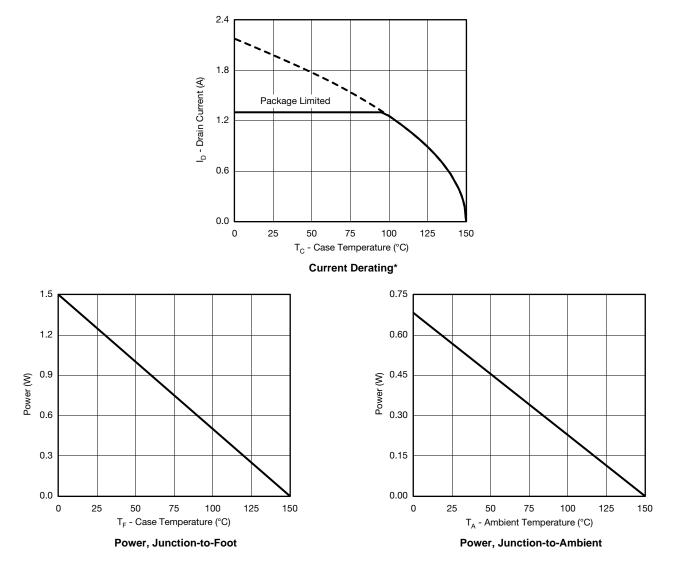


T_J - Temperature (°C) Threshold Voltage





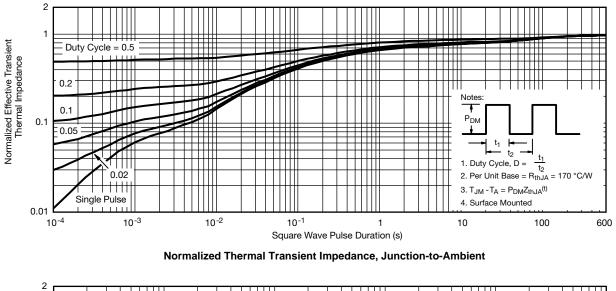
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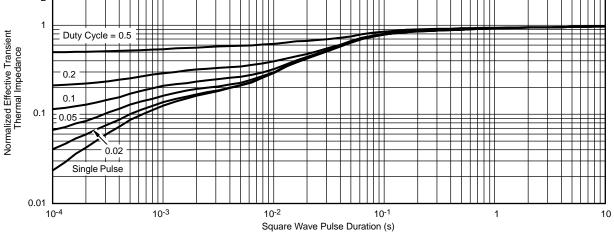


* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)



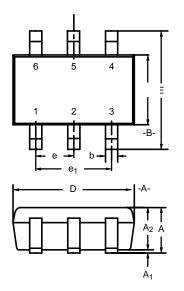


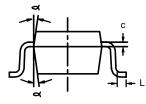
Normalized Thermal Transient Impedance, Junction-to-Foot

NTJD4401NT1G



SC-70: 6-LEADS





	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
A ₁	-	-	0.10	-	-	0.004	
A ₂	0.80	-	1.00	0.031	-	0.039	
b	0.15	-	0.30	0.006	-	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Е	1.80	2.10	2.40	0.071	0.083	0.094	
E ₁	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC			0.026BSC			
e ₁	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
م	7°Nom			7°Nom			



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