

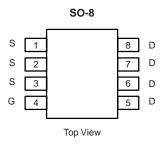
AP9575GM P-Channel 60 V (D-S) MOSFET

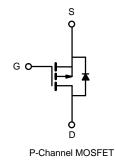
PRODUCT SUMMARY				
V _{DS} (V)	-60			
$R_{DS(on)}(\Omega)$ at V_{GS} = -10 V	0.050			
$R_{DS(on)}$ (Ω) at V_{GS} = -4.5 V	0.060			
I _D (A) per leg	-8			

FEATURES

- TrenchFET[®] power MOSFET
- 100 % R_g and UIS tested







ABSOLUTE MAXIMUM RATING	3S (T _C = 25 °C, unless	otherwise noted	l)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	-60		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C	I	-8		
	T _C = 125 °C	Ι _D	-4.75		
Continuous Source Current (Diode Conduction)		I _S	-4.5	А	
Pulsed Drain Current ^a		I _{DM}	-32		
Single Pulse Avalanche Current		I _{AS}	-22.4		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	25	mJ	
	T _C = 25 °C	T _C = 25 °C		14/	
Maximum Power Dissipation ^a	T _C = 125 °C	PD	1.67	W	
Operating Junction and Storage Temperature Range		TJ, T _{sta}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	110	°C/W		
Junction-to-Foot (Drain)		R _{thJF}	30	0,00		

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. When mounted on 1" square PCB (FR-4 material).

c. Parametric verification ongoing.

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	+	•					
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \ \mu A$		-60	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$		-1.0		-2.5	
Gate-Source Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = -60 V	-	-	-1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V_{DS} = -60 V, T _J = 125 °C	-	-	-50	
		$V_{GS} = 0 V$	V _{DS} = -60 V, T _J = 175 °C	-	-	-150	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = -10 V$	$V_{DS} \le -5 V$	-30	-	-	А
		$V_{GS} = -10 V$	I _D = -4.3 A	-	0.050	-	Ω
Durin Course On Otata Desistance 3	В	$V_{GS} = -10 \text{ V}$	$I_D = -4.3 \text{ A}, \text{ T}_J = 125 ^\circ\text{C}$	-	0.070	-	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -10 \text{ V}$	I _D = -4.3 A, T _J = 175 °C	-	0.080	-	
		$V_{GS} = -4.5 V$	I _D = -3.8 A	-	0.060	-	
Forward Transconductance b	9 _{fs}	V _{DS} = -15 V, I _D = -4.3 A		-	13	-	S
Dynamic ^b	·	·					
Input Capacitance	C _{iss}		_{3S} = 0 V V _{DS} = -30 V, f = 1 MHz	-	1530	1910	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	334	417	
Reverse Transfer Capacitance	C _{rss}			-	114	142	
Total Gate Charge ^c	Qg			-	43.4	65	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = -10 V	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	-	4.7	-	nC
Gate-Drain Charge ^c	Q _{gd}			-	9	-	1
Gate Resistance	Rg		f = 1 MHz		2.5	4	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	11	17	
Rise Time ^c	t _r	V_{DD} = -30 V, R _L = 8.8 Ω I _D \cong -5 A, V _{GEN} = -10 V, R _g = 1 Ω		-	11	17	- ns
Turn-Off Delay Time ^c	t _{d(off)}			-	35	52	
Fall Time ^c	t _f			-	6	9	
Source-Drain Diode Ratings and Char	acteristics ^b					1	
Pulsed Current ^a	I _{SM}			-	-	-32	Α
Forward Voltage	V _{SD}	I _F =	-2.8 A, V _{GS} = 0 V	-	-0.8	-1.2	V
					I		

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

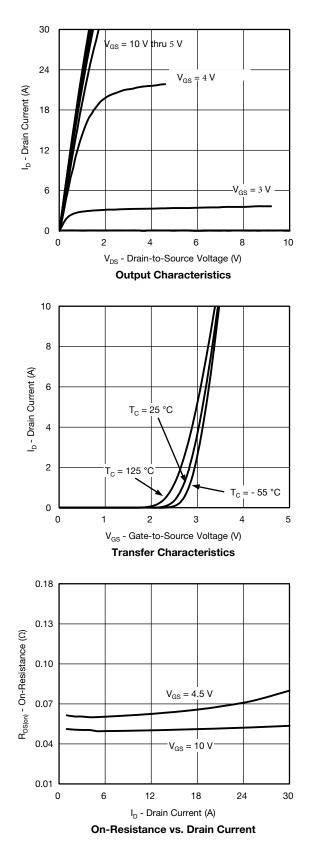
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

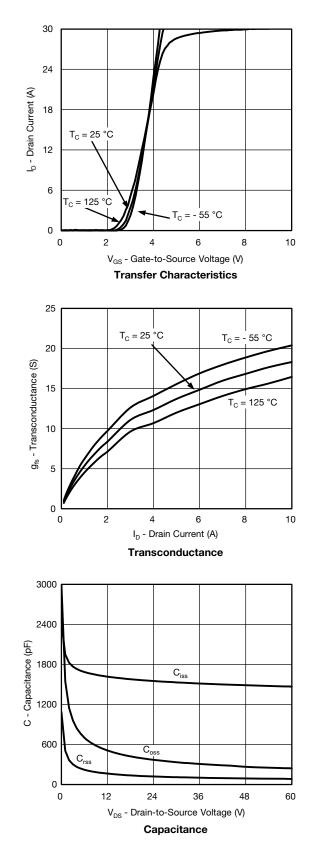
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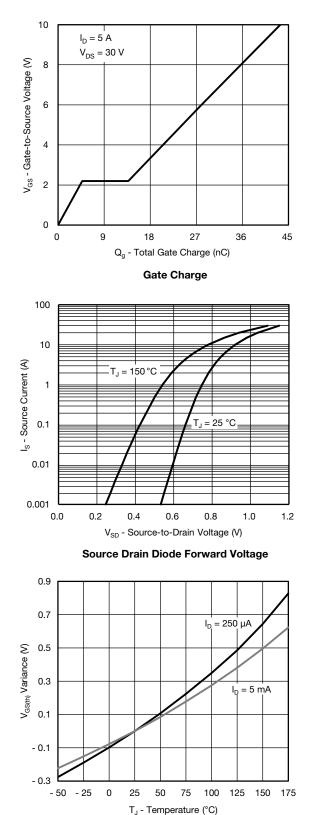
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



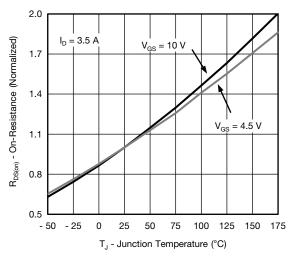




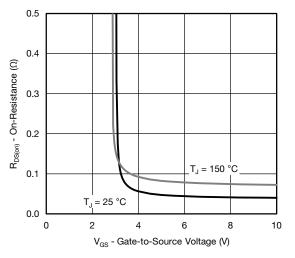
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



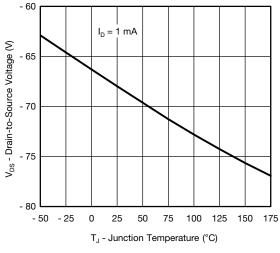




On-Resistance vs. Junction Temperature



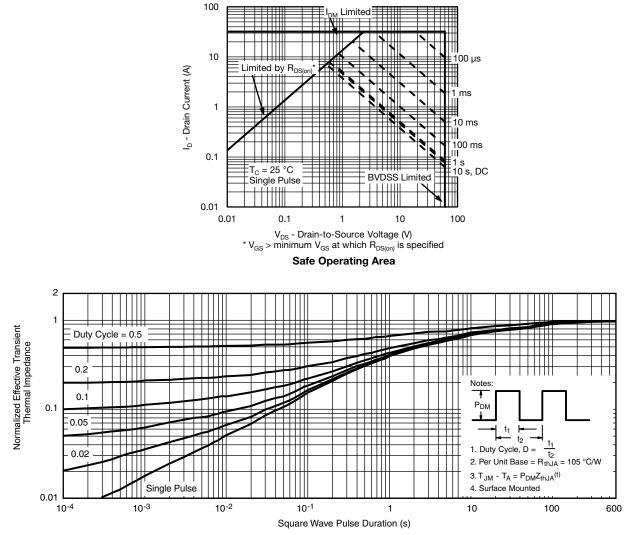
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



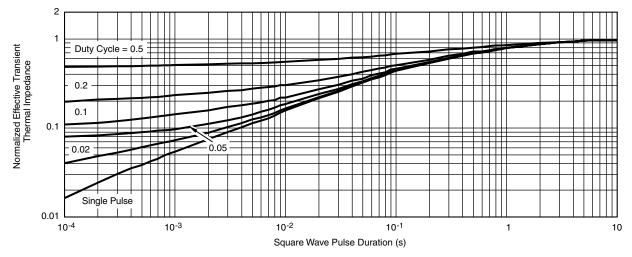
THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

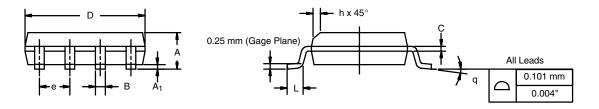
The characteristics shown in the two graphs





SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012

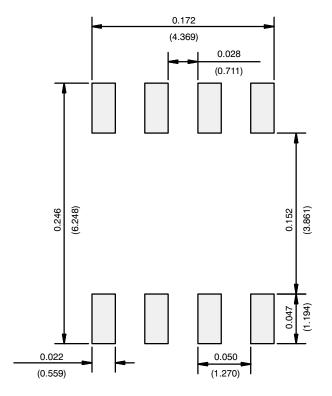




	MILLIM	IETERS	INC	HES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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